

FIG. 1

FIG. 2

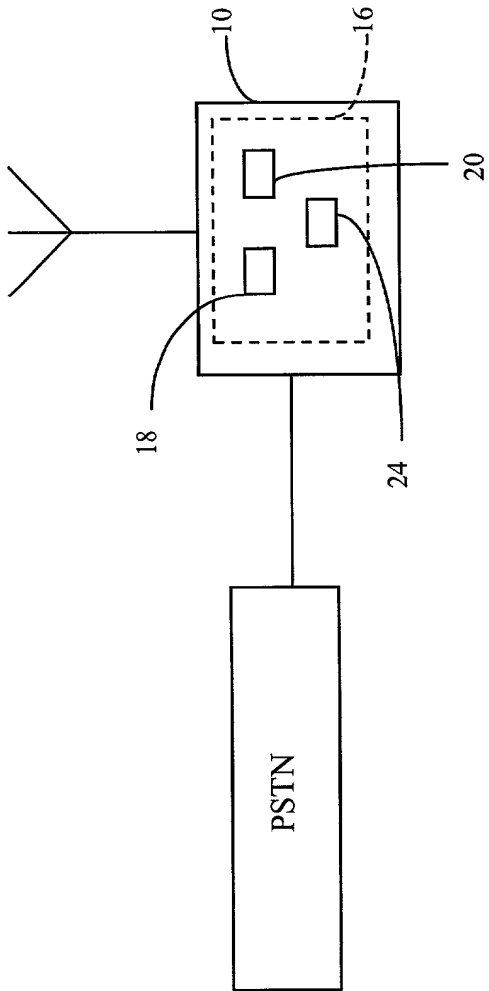


FIG. 2

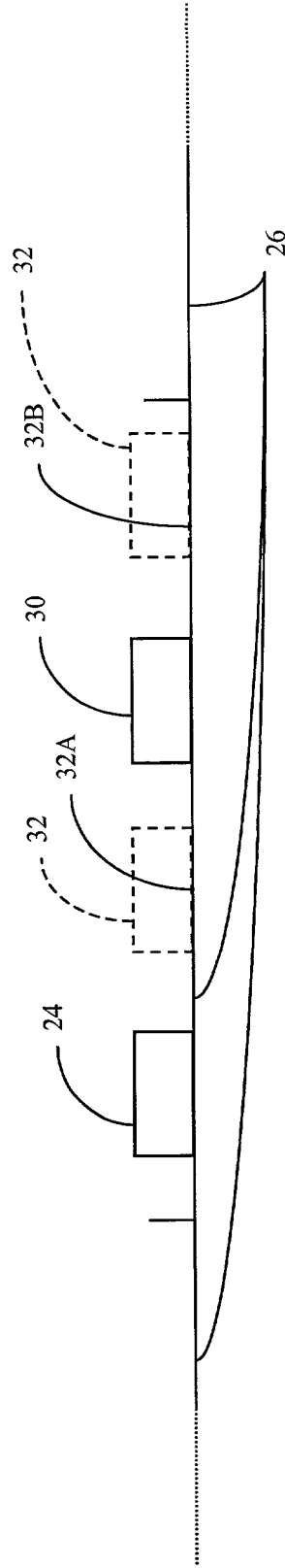


FIG. 3

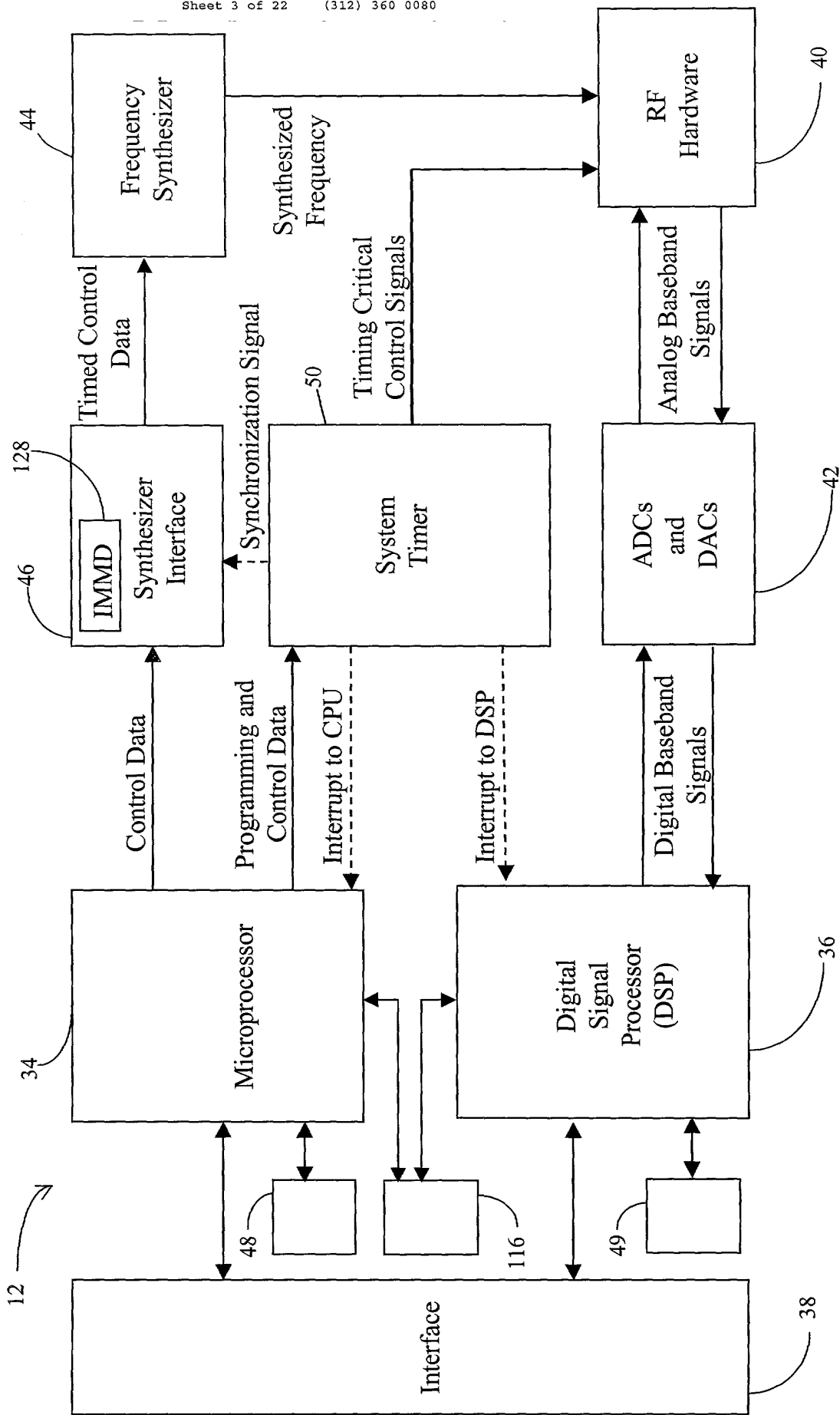


FIG. 4

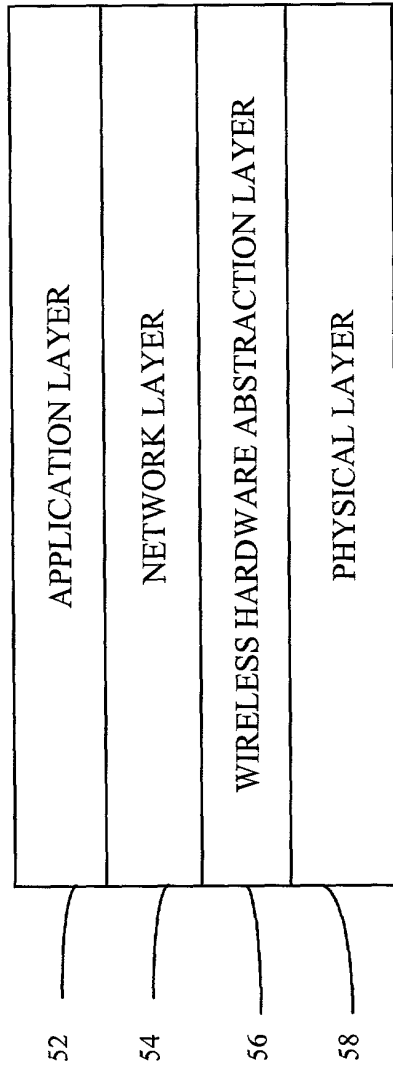


FIG. 5

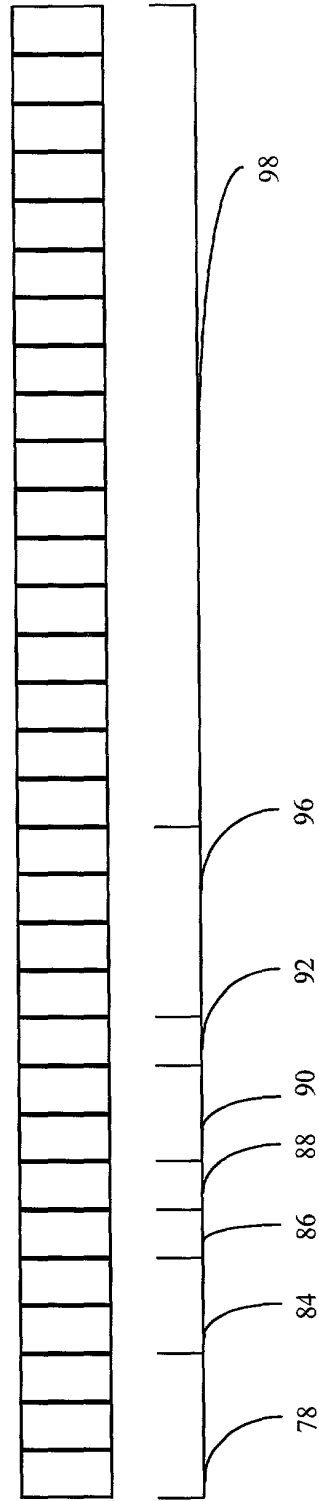


FIG. 7

FIG. 6

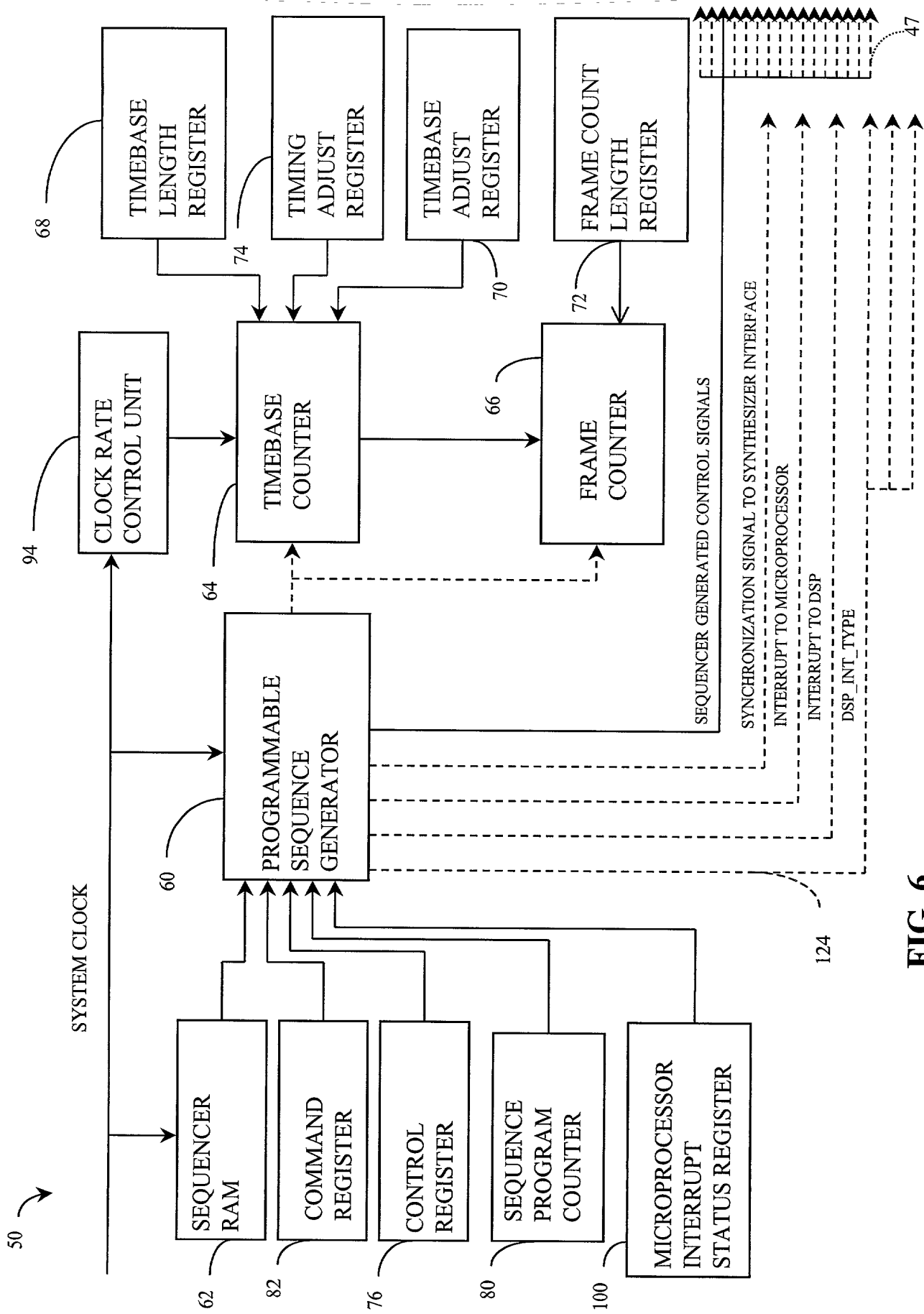


FIG. 6

FIG. 8

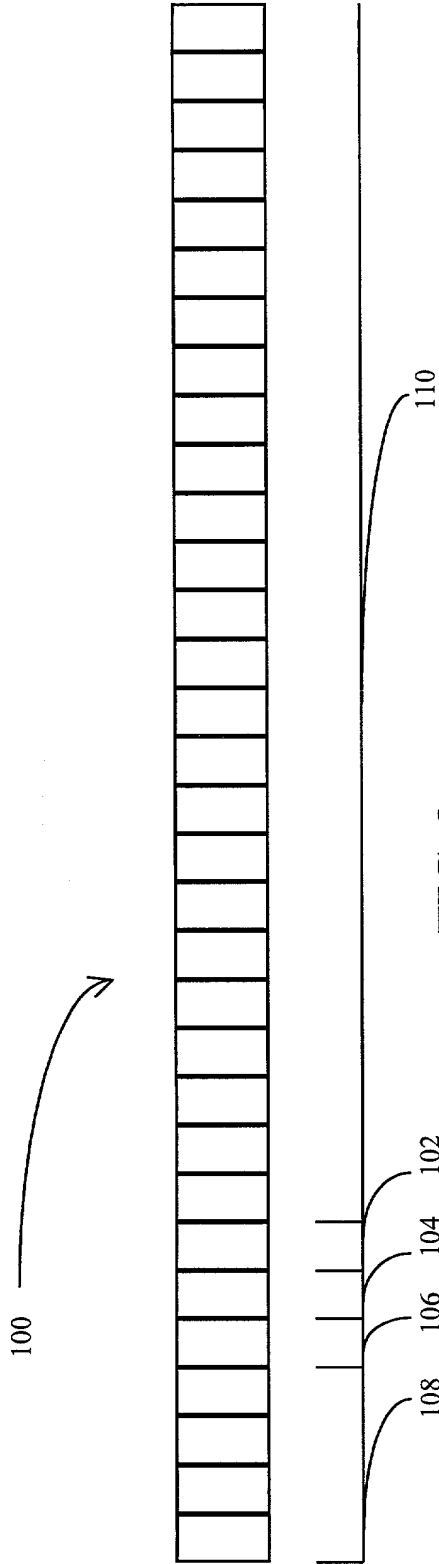


FIG. 8

146	TYPES OF DSP INTERRUPTS
148	INVALID
150	FRAME
152	RXWIN
154	TXWIN
156	HANDOFF
158	APC
157	TXWINOFF
	RESERVED

FIG. 10

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	INSTRUCTION	FORMAT
112	JCMD	JCMD aaaa aaaa
114	JMP	JMP ABCD aaaa aaaa
117	SET	SET m, k, j
118	CLR	CLR m, k, j
120	DSPINT	DSPINT tttt
122	ARMINT	ARMINT tttt
126	SYNSEND	SYNSEND
132	RXENA	RXENA
133	RXDIS	RXDIS
134	TXSTART	TXSTART
136	RTBWAIT	RTBWAIT w
138	FRWAIT	FRWAIT w
140	TBWAIT	TBWAIT w
142	TBADJSN	TBADJSN
144	NOP	NOP

FIG. 9

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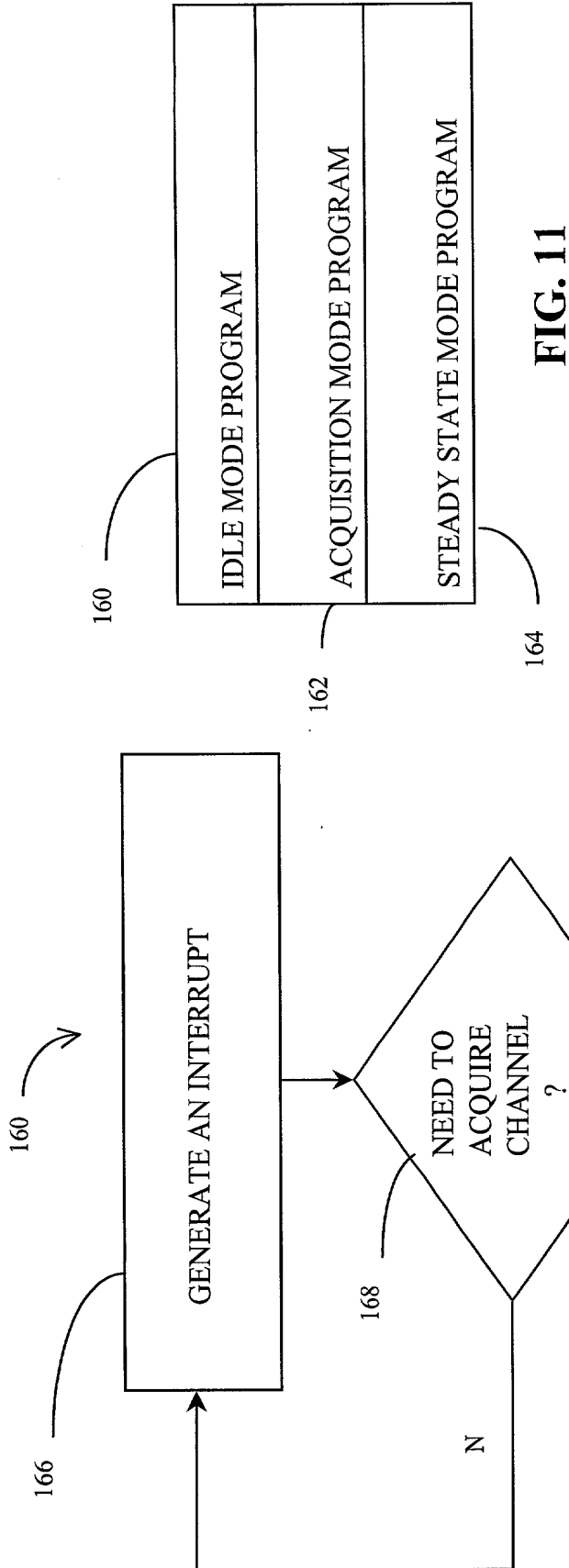


FIG. 12

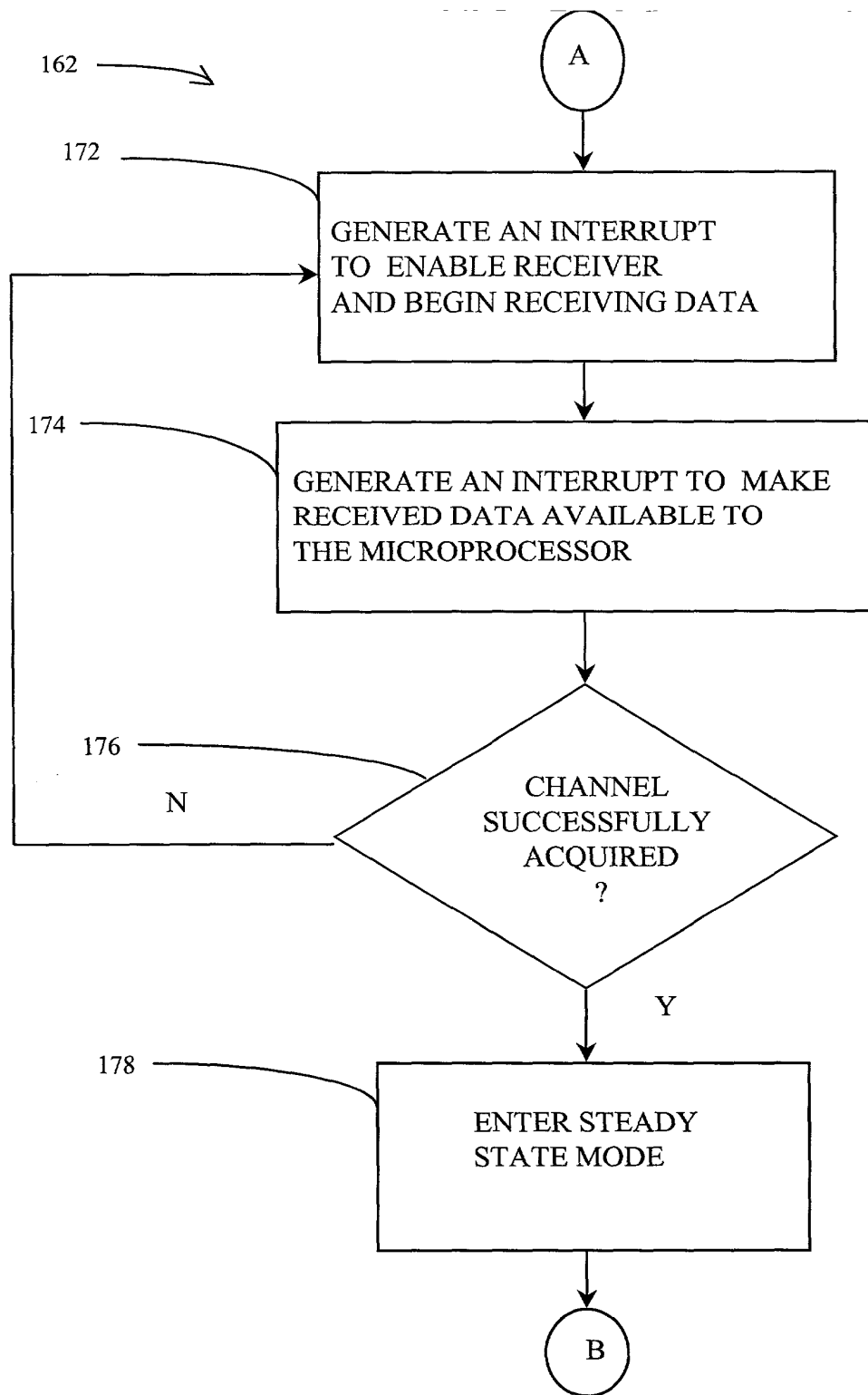


FIG. 13

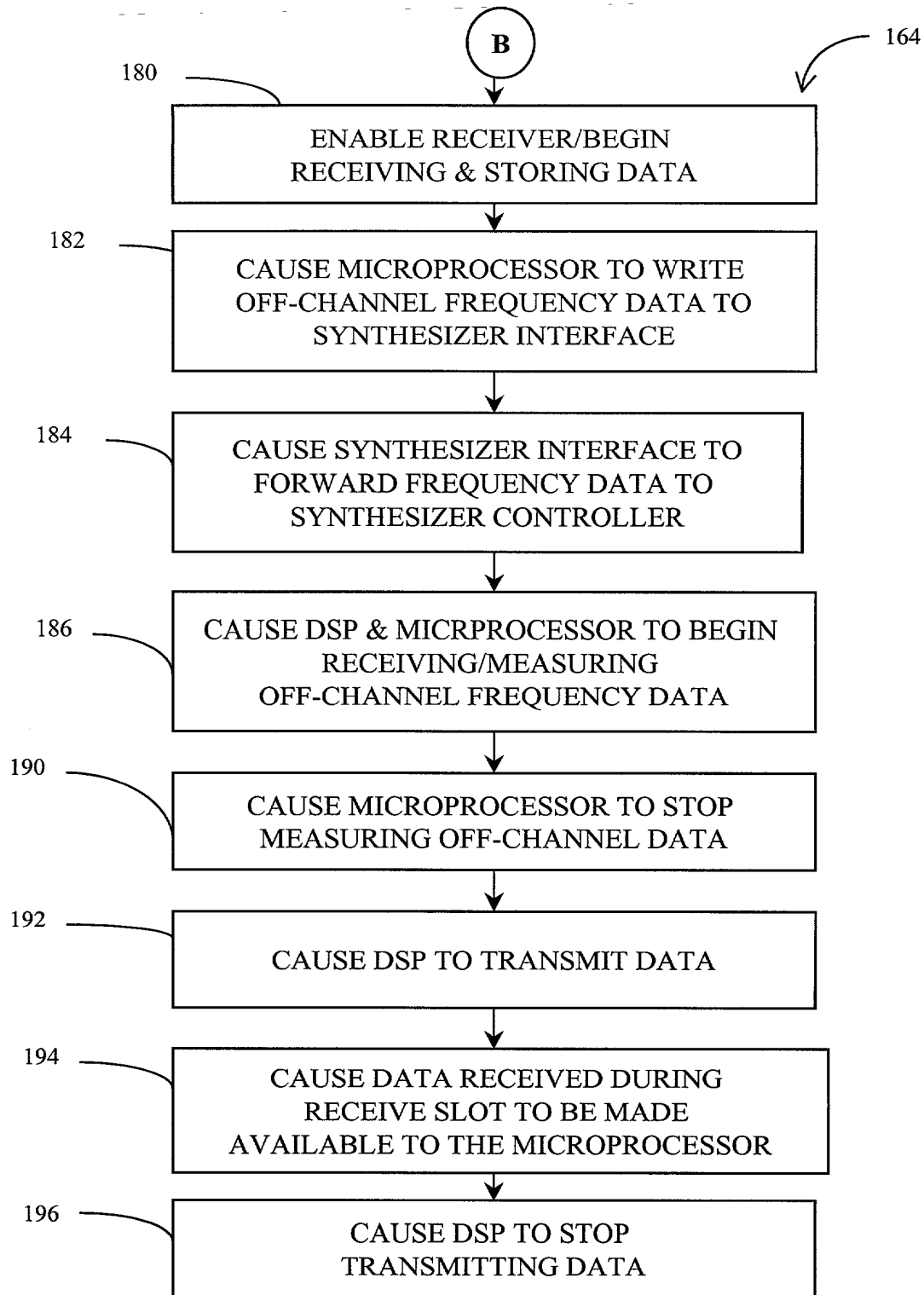


FIG. 14

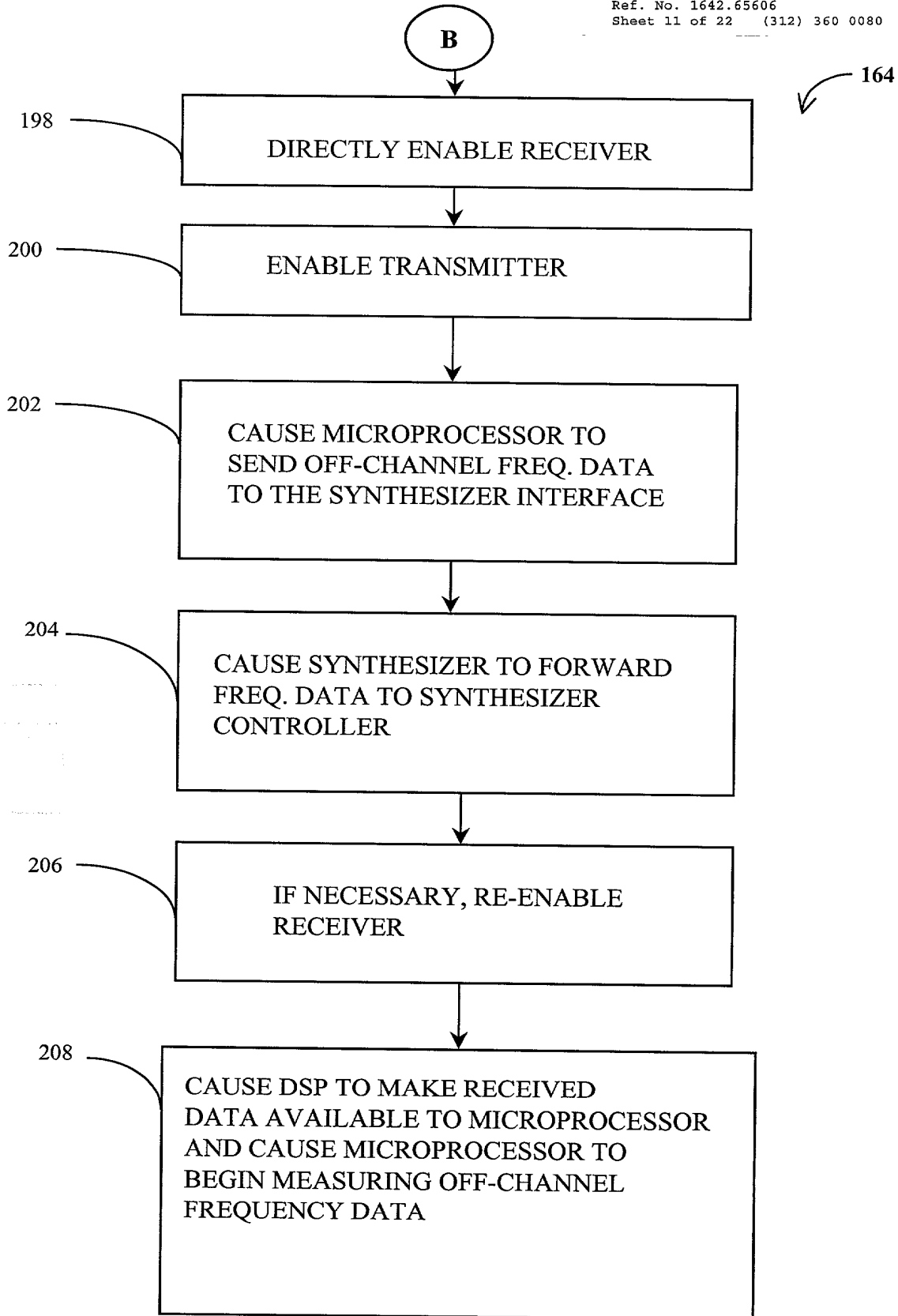


FIG. 15

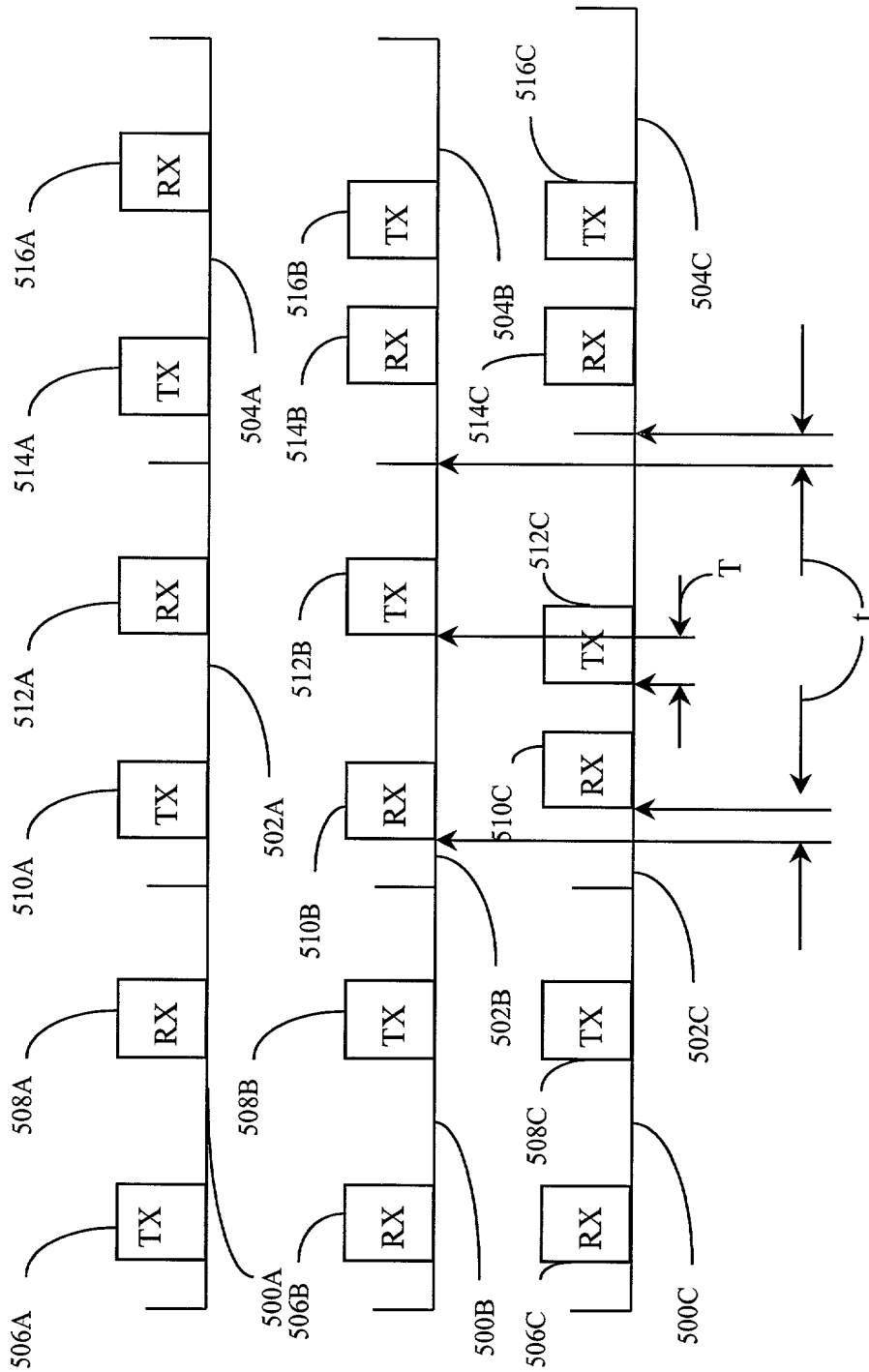


FIG. 16

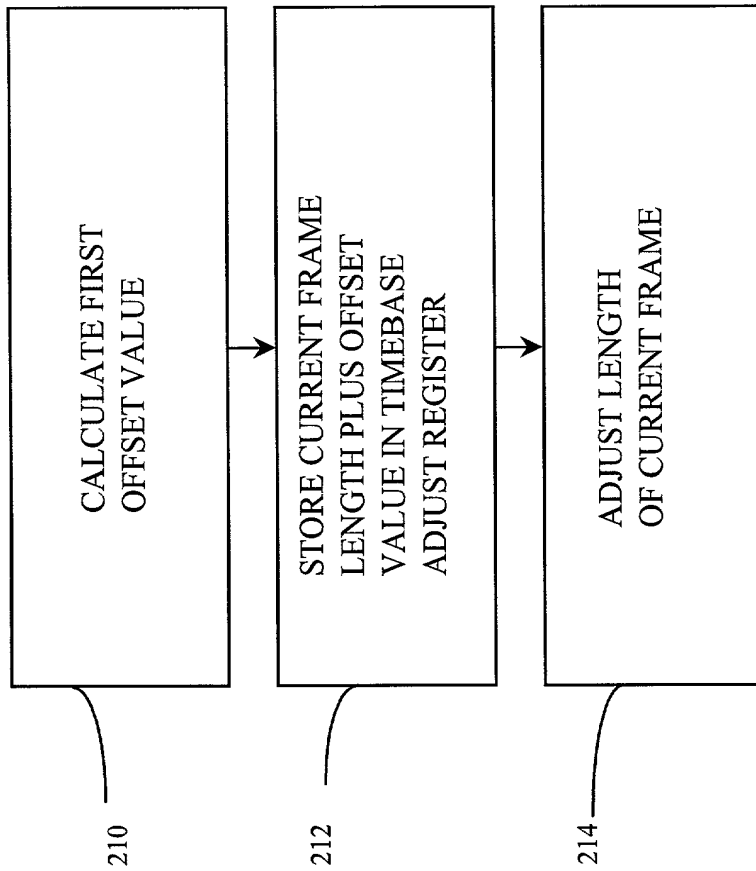


FIG. 17

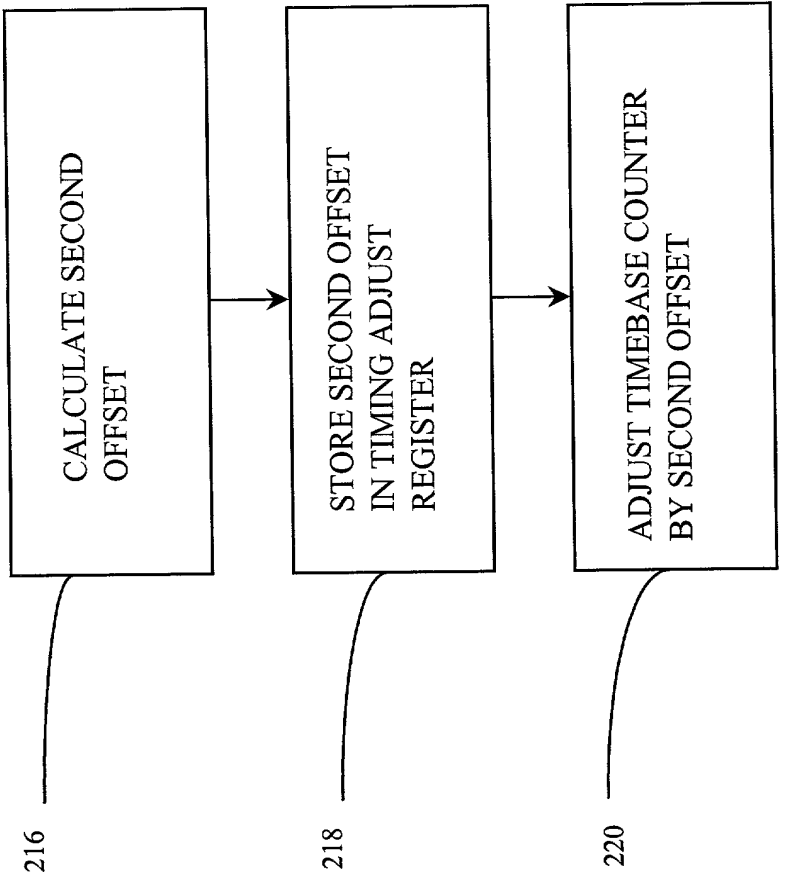


FIG. 18

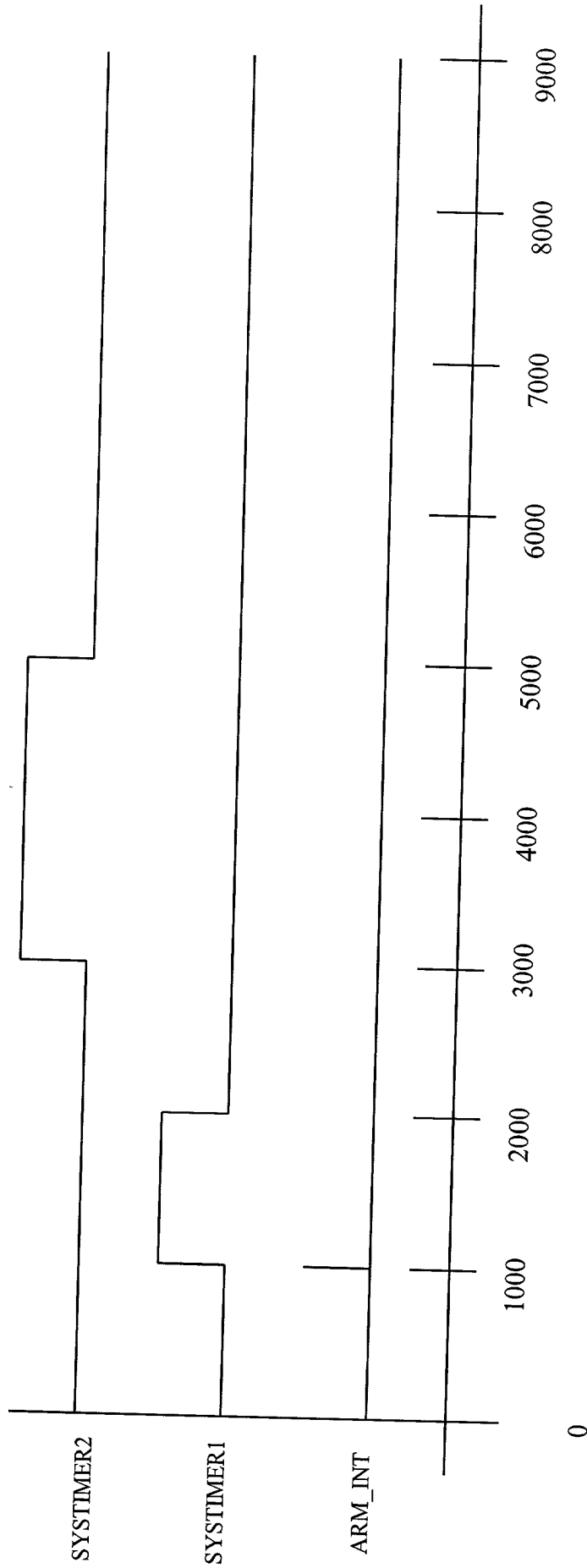


FIG. 19

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SEQUENCER RAM ADDRESS	INSTRUCTION
0	TBWAIT 1000
1	ARMINT 3
2	SET 1,1,1
3	TBWAIT 2000
4	CLR 1,1,1
5	TBWAIT 3000
6	SET 2,2,2
7	TBWAIT 5000
8	CRL 2,2,2
9	JCMD 0

FIG. 20

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SEQUENCER RAM ADDRESS	INSTRUCTION	
0	JMP 1000, 6	246
1	TBWAIT 1000	248
2	ARMINT 3	250
3	SET 1, 1, 1	252
4	TBWAIT 2000	254
5	CLR 1, 1, 1	256
6	JMP 0100, 11	258
7	TBWAIT 3000	260
8	SET 2, 2, 2	262
9	TBWAIT 2000	264
10	CLR 2, 2, 2	266
11	JMP 0010, 16	268
12	TBWAIT 6000	270
13	SET 2, 2, 2	272
14	TBWAIT 6500	274
15	CLR 2, 2, 2	276
16	JMP 0001, 21	278
17	TBWAIT 7000	280
18	SET 2, 2, 2	282
19	TBWAIT 7500	284
20	CLR 2, 2, 2	286
21	JCMD 0	288

FIG. 21

SEQUENCER RAM ADDRESS	INSTRUCTION
0	JCMD 1
1	TBWAIT 1000
2	ARMINT 3
3	SET 1, 1, 1
4	RTBWAIT 1000
5	JCMD 6
6	CLR 1, 1, 1
7	JCMD 8
8	TBADJSN 0
9	RTBWAIT 2000
10	SET 2, 2, 2
11	RTBWAIT 1000
12	JCMD 13
13	CLR 2, 2, 2
14	TBADJSN 1
15	JCMD 0

FIG. 22

FIRST PROGRAM

0	TBWAIT 50	324
1	JCMD 0	326

RXSTART PROGRAM

2	RTBWAIT 30	330
3	SET 1, 1, 1	332
4	RTBWAIT 20	334
5	RXENA	336
6	RTBWAIT 50	338
7	JCMD 0	340

RXEND PROGRAM

8	RTBWAIT 50	344
9	RXDIS	346
10	RTBWAIT 20	348
11	CLR 1, 1, 1	350
12	RTBWAIT 30	352
13	JCMD 0	354

TXSTART PROGRAM

14	RTBWAIT 29	358
15	SET 2, 2, 2	360
16	RTBWAIT 20	362
17	TXSTART	364
18	RTBWAIT 50	366
19	JCMD 0	368

FIG. 23A

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TXEND PROGRAM 372

20	RTBWAIT 70	374
21	CLR 2, 2, 2	376
22	RTBWAIT 30	378
23	JCMD 0	380

REPEAT PROGRAM 382

24	RTBWAIT 100	384
25	JCMD 0	386

LAST PROGRAM 388

26	RTBWAIT 50	390
27	JCMD 0	392

ADVANCE PROGRAM 394

28	TBADJSN 0	396
29	JCMD 0	398

RETARD PROGRAM 400

30	TBADJSN 1	402
31	JCMD 0	404

FIG. 23B

FIG. 25

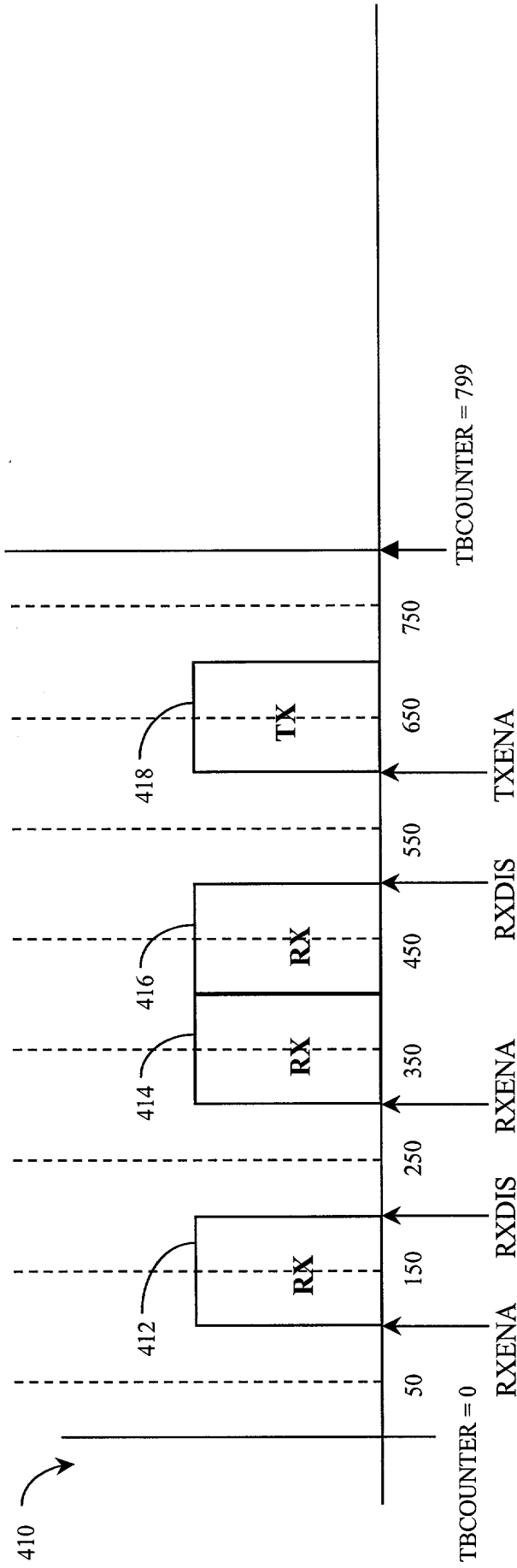


FIG. 24

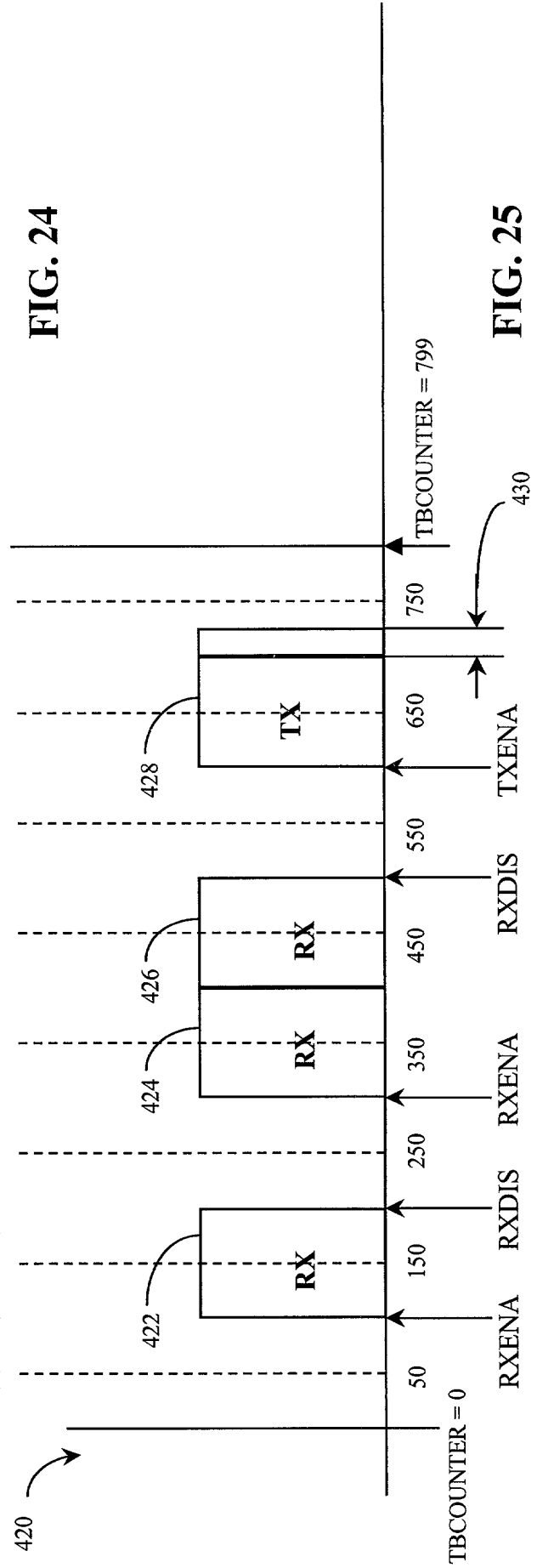


FIG. 25

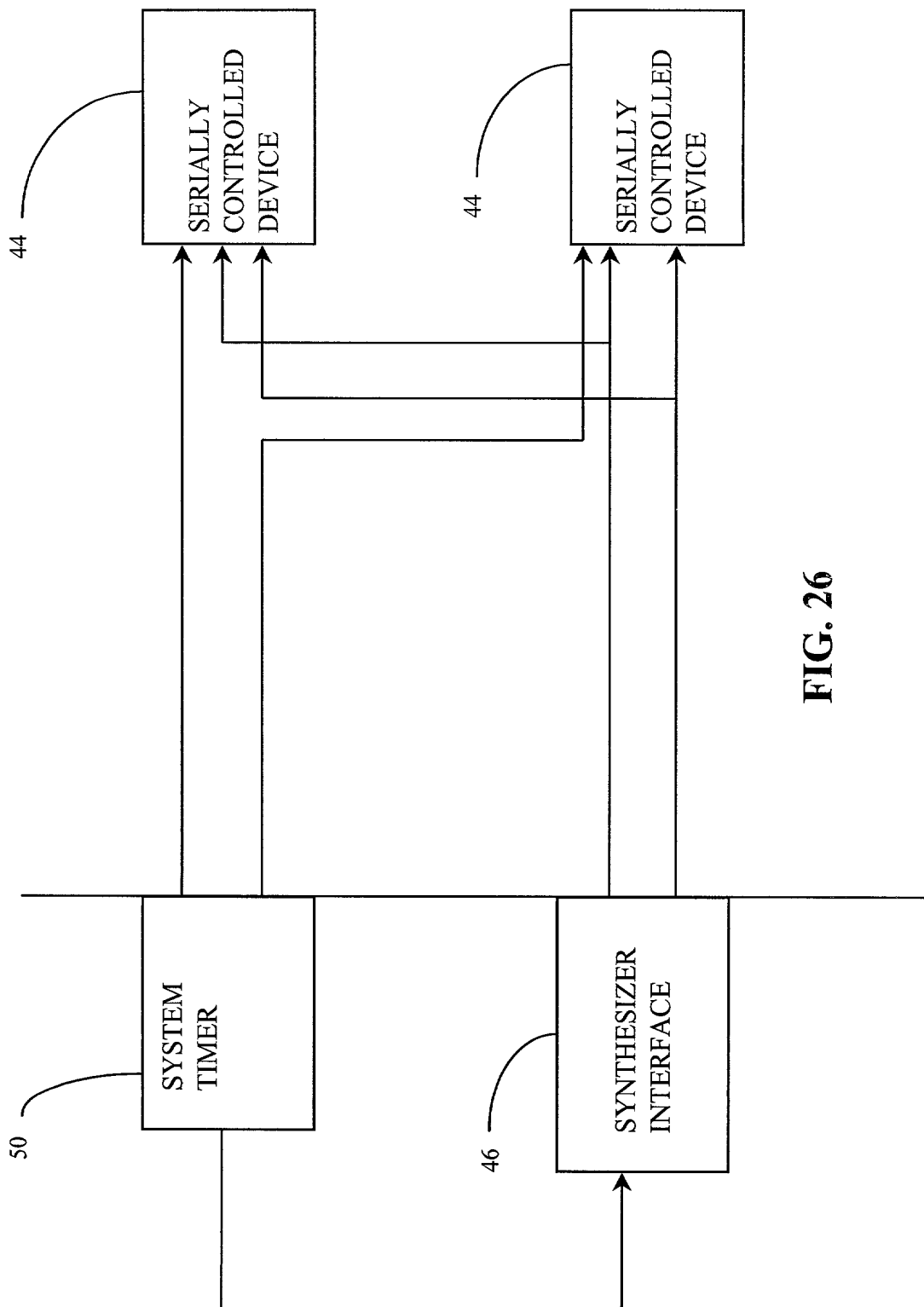


FIG. 26

TO 4290" 48626360